

----- Boolean Equation Segment -----

EQUATIONS

/WR_NLATCH = /CS_WAVE * /RW * /WU * A12 * A13 * /A14

/R_WE = /CS_WAVE * /RW * /WU * /A12 * /A13 * /A14

/G_WE = /CS_WAVE * /RW * /WU * A12 * /A13 * /A14

/B_WE = /CS_WAVE * /RW * /WU * /A12 * A13 * /A14

/GATE_R = /CS_WAVE * /A12 * /A13 * /A14

/GATE_G = /CS_WAVE * A12 * /A13 * /A14

/GATE_B = /CS_WAVE * /A12 * A13 * /A14

IOO = GND

----- Simulation Segment -----

SIMULATION

TRACE_ON A14 A13 A12 RW WU CS_WAVE

WR_NLATCH R_WE G_WE B_WE GATE_R GATE_G GATE_B

SETF /A14 /A13 /A12 RW WU /CS_WAVE

SETF /A14 A13 /A12

SETF /A14 A13 A12

SETF CS_WAVE

SETF /CS_WAVE

SETF /RW

SETF /WU

SETF WU A13 /A12

SETF /WU

SETF WU /A13 A12

SETF /WU

SETF WU /A13 /A12

SETF /WU

SETF WU

SETF CS_WAVE

TRACE_OFF
